

Refine Search

Search Results -

Terms	Documents
(709/208 709/253 710/110 710/107 710/113 710/309 710/240 370,461,462/).ccls.	2148

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L1

Refine Search**Recall Text****Clear****Interrupt**

Search History

DATE: Monday, April 19, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

*DB=USPT; PLUR=YES; OP=OR*L1 710/110,107,113,309,240;709/208,253;370,461,462.ccls.**Hit Count Set Name**

result set

2148 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L7	41

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Search History

DATE: Monday, April 19, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=USPT; PLUR=YES; OP=OR

L8 l1 and L7L7 L2 same period same requestL6 l1 and L5L5 L2 same (period or tim\$3) same requestL4 l1 and L3L3 L2 same (period or tim\$3)L2 (bus adj1 (master or controller)) same arbit\$6L1 710/110,107,113,309,240;709/208,253;370,461,462.ccls.

Hit Count Set Name

result set

41 L8142 L7180 L6685 L5251 L41099 L32249 L22148 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L2 and ((target or destination) adj1 address)	0

Database:

US Pre-Grant Publication Full-Text Database
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 US OCR Full-Text Database
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 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L3

Search History

DATE: Monday, April 19, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side

Hit Count Set Name
result set

DB=USPT; PLUR=YES; OP=OR

<u>L3</u>	L2 and ((target or destination) adj1 address)	0	<u>L3</u>
<u>L2</u>	6070205.pn. and ((address near5 decod\$3) same arbit\$6)	1	<u>L2</u>
<u>L1</u>	6070205.pn. and (address near5 decod\$3)	1	<u>L1</u>

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File

View

Edit

Tools

Window

Help

Drafts

Pending

Active

L1: (1) (first adj1 bus adj1

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search

List

Browse

Queue

Clear

DBs

USPAT

Plurals

Default operator:

OR

Highlight all hit terms initially

BRS I...

IS&R ...

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err
1	BRS	L1	1	(first adj1 bus adj1 (master or controller))	USPAT	2004/04/16 09:09			0

Start

EAST - [Untitled1:1]

EAST - [Untitled1:1]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

L1: (1) (first adj1 bus adj1

SearchListBrowseQueueClear

DBsUSPATPlurals

Default operator: ORHighlight all hit terms initially

(first adj1 bus adj1 (master or controller)) same (second adj1 bus adj1 (master or controller)) same (first adj3 arbit\$6) same (second adj3 arbit\$3)

BRS I...IS&R ...ImageTextHTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R
1	<input type="checkbox"/>	<input type="checkbox"/>	US 5970234 A	19991019	17	PCI bus arbiter and a bus control system having the	710/111	710/240; 710/243;	

StartEAST - [Untitled1:1]

(first adj1 bus adj1 (master or controller)) same (second adj1 bus adj1 (master or controller)) same (first adj3 arbit\$6) same (second adj3 arbit\$3)

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R
1	<input type="checkbox"/>	<input type="checkbox"/>	US 5970234 A	19991019	17	PCI bus arbiter and a bus control system having the	710/111	710/240; 710/243;	